ABSTRACT OF THE DISCLOSURE

In a parameter correction circuit to be included in an LSI, a reference resistor element with high precision having a resistance value set to a target value is connected to the external terminal of the LSI. A constant current I1 is allowed to flow from a mirror circuit connected to a current supply to the reference resistor element so that a voltage value generated in the reference resistor element is measured by a voltage measuring circuit. Next, a constant current I1 is allowed to flow in the same manner from the mirror circuit to a variable resistor element to be adjusted and corrected, and at this time, the resistance value of the variable resistor element is adjusted so that a voltage generated in the variable resistor element is allowed to correspond to the voltage generated in the reference resistor element. Therefore, when the absolute precision of the constant current I1 of the current supply is not available due to production deviations and the like, it becomes possible to adjust and correct the resistance value of the variable resistor element to a target value with high precision.